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Robin E. Barnum	(Depositor's name)
	(Signature)
February 27, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/943,598	08/30/2001	Gary L. Swoboda	TI-30480	2621

TITLE OF INVENTION: SYNCHRONIZING ON-CHIP DATA PROCESSOR TRACE AND TIMING INFORMATION FOR EXPORT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/15/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
GUILL, RUSSELL L	2123	703-028000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.563).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
- (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Robert D. Marshall, Jr.2 W. James Brady, III3 Frederick J. Telecky, Jr.

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(A) NAME OF ASSIGNEE

Texas Instruments Incorporated

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Dallas, TX USA

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
- ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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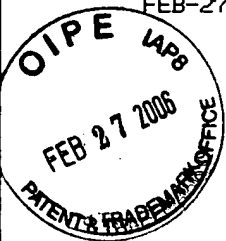
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28,527

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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30480

Gary L. Swoboda, et al.

Art Unit: 2123

Serial No.: 09/943,598

Examiner: Russell L. Guill


Filed: August 30, 2001

Conf. No.: 2621

For: Synchronizing On-Chip Data Processor Trace and Timing Information for Export

#### CERTIFICATION OF FACSIMILE TRANSMISSION

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<table border="1"> <tr> <td colspan="2">NAME OF INVENTOR(S): Gary L. Swoboda, et al.</td> </tr> <tr> <td colspan="2">TITLE OF INVENTION: Synchronizing On-Chip Data Processor Trace and Timing Information for Export</td> </tr> <tr> <td>TI FILE NO.: <b>TI-30480</b></td> <td>DEPOSIT ACCT. NO.: <b>20-0668</b></td> </tr> <tr> <td colspan="2">           FAXED: 2/27/06            DUE: 5/15/06            ATTY/SECY: RDM/reb         </td> </tr> </table>		NAME OF INVENTOR(S): Gary L. Swoboda, et al.		TITLE OF INVENTION: Synchronizing On-Chip Data Processor Trace and Timing Information for Export		TI FILE NO.: <b>TI-30480</b>	DEPOSIT ACCT. NO.: <b>20-0668</b>	FAXED: 2/27/06 DUE: 5/15/06 ATTY/SECY: RDM/reb	
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